PEER REVIEW OF PCB DISCOVERY HAT

# Adherence TO Mr. Pead’s basic requirements

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Unacceptable  1 | Ok attempt  2 | Reasonable  3 | Good  4 | Excellent  5 |
|  |  |  |  |  |
| Comment: | | | | |

# Adherence TO Mr. Pead’s debugger requirements

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Unacceptable  1 | Ok attempt  2 | Reasonable  3 | Good  4 | Excellent  5 |
|  |  |  |  |  |
| Comment: | | | | |

# Schematic

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Unacceptable  1 | Ok attempt  2 | Reasonable  3 | Good  4 | Excellent  5 |
|  |  |  |  |  |
| Comment: | | | | |

# PCB Layout

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Unacceptable  1 | Ok attempt  2 | Reasonable  3 | Good  4 | Excellent  5 |
|  |  |  |  |  |
| Comment: | | | | |

# Silk Screen

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Unacceptable  1 | Ok attempt  2 | Reasonable  3 | Good  4 | Excellent  5 |
|  |  |  |  |  |
| Comment: | | | | |

# Low voltage protection circuit

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Unacceptable  1 | Ok attempt  2 | Reasonable  3 | Good  4 | Excellent  5 |
|  |  |  |  |  |
| Comment: | | | | |

# Physical board design

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Unacceptable  1 | Ok attempt  2 | Reasonable  3 | Good  4 | Excellent  5 |
|  |  |  |  |  |
| Comment: | | | | |

# Test points and recovery approaches

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Unacceptable  1 | Ok attempt  2 | Reasonable  3 | Good  4 | Excellent  5 |
|  |  |  |  |  |
| Comment: | | | | |